

PATENT APPLICATION  
DOCKET NO.: 200208996-1

IN THE SPECIFICATION

Please replace Paragraph [0002] with the following amended paragraph:

[0002] This application is related to U.S. Patent Application Serial No. \_\_\_\_\_, filed \_\_\_\_\_ entitled Serial No. 10/635,083 entitled GENERAL PURPOSE PERFORMANCE COUNTER, (~~Docket No. 200208999-2~~); U.S. Patent Application Serial No. \_\_\_\_\_, filed \_\_\_\_\_ entitled Serial No. 10/635,369 entitled INCREMENT/DECREMENT CIRCUIT FOR PERFORMANCE COUNTER, (~~Docket No. 200208998-1~~); U.S. Patent Application Serial No. \_\_\_\_\_, filed \_\_\_\_\_ entitled Serial No. 10/365,372 entitled COVERAGE DECODER CIRCUIT FOR PERFORMANCE COUNTER, (~~Docket No. 200208997-1~~); U.S. Patent Application Serial No. \_\_\_\_\_, filed \_\_\_\_\_ entitled Serial No. 10/635,103 entitled DATA SELECTION CIRCUIT FOR PERFORMANCE COUNTER, (~~Docket No. 200209000-1~~); U.S. Patent Application Serial No. \_\_\_\_\_, filed \_\_\_\_\_ entitled Serial No. 10/635,079 entitled ZEROING CIRCUIT FOR PERFORMANCE COUNTER, (~~Docket No. 200209001-1~~); and U.S. Patent Application Serial No. \_\_\_\_\_, filed \_\_\_\_\_ entitled Serial No. 10/635,373 entitled MATCH CIRCUIT FOR PERFORMANCE COUNTER, (~~Docket No. 200209002-1~~), each of which

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was filed on August 6, 2003 and all of which are hereby  
incorporated by reference in their entirety.